

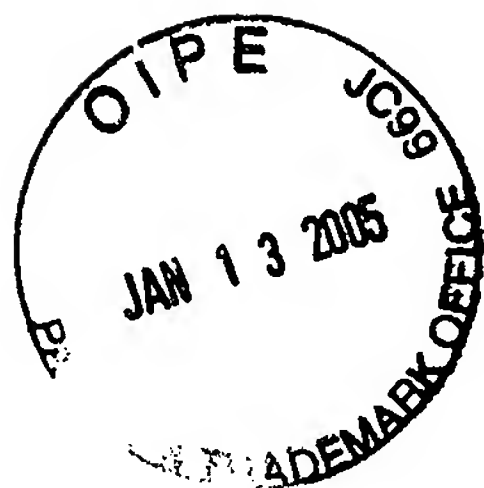
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IPW

ANDREWS
ATTORNEYS **KURTH** LLP

111 Congress Avenue, Suite 1700
Austin, Texas 78701
512.320.9200 Phone
512.320.9292 Fax
andrewskurth.com

Nathan H. Calvert
512/320.9291 Direct
Nathancalvert@andrewskurth.com



January 13, 2005

VIA EXPRESS MAIL NO. EV343331334US

MAIL STOP - AMENDMENT
Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

RE: Application No.: 10/709,732
Filing Date: May 25, 2004
Title: Memory Expansion and Chip Scale Stacking System and
and Method
Docket No.: 254-094-CIP4-CMEM
AK Matter No.: 153715

Dear Commissioner:

Please find enclosed for filing the following:

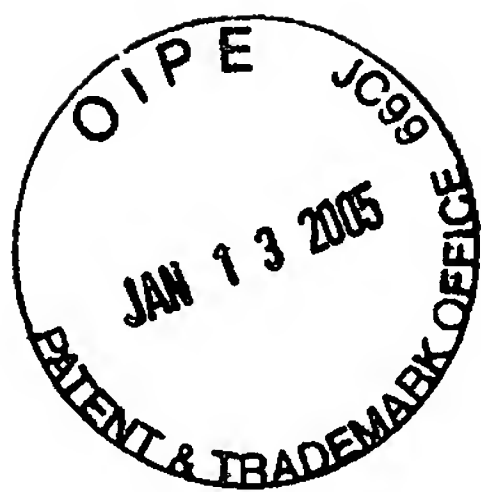
1. Information Disclosure Statement (2 pages);
3. Forms PTO/SB/08 (9 sheets) (with duplicate copy);
4. 16 references; and
5. a return postcard for confirmation of receipt.

The Commissioner is hereby authorized to charge any fees which may be required to Deposit Account 50-0897. Please return the postcard confirming your receipt of the enclosed materials.

Very truly yours,

Nathan H. Calvert

NHC/at
Enclosures



Attorney Docket No.: 254-094-CIP4-CMEM

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Title: Memory Expansion and Chip Scale Stacking System and Method
Application No.: 10/709,732
Filing Date: May 25, 2004
Inventor: Russell Rapport, et al.
Assignee: Staktek Group L.P.
Examiner: TBA
Art Unit: 2811

INFORMATION DISCLOSURE STATEMENT

Mail Stop - Amendment
Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

Pursuant to the guidelines for Information Disclosure Statements, 37 C.F.R. §§ 1.97-1.98, attached hereto are Forms PTO/SB/08 (9 sheets) with references listed for consideration by the Office.

Pursuant to Official Gazette Notice No. 14, dated August 5, 2003, copies of the listed written U.S. references are not enclosed. However, in accordance with 37 C.F.R. § 1.98(a)(2), copies of the listed written foreign references and articles are being provided.

In accordance with 37 C.F.R. §§ 1.97(g)-(h), the filing of this Information Disclosure Statement shall not be construed as a representation that a search has been made or an admission that the information cited in the statement is, or is considered to be, material to patentability as defined in 37 C.F.R. § 1.56(b) or prior art under 35 U.S.C. §§ 102 or 103.

The examiner should also be aware of the co-pending applications in the same application family as the above-referenced application. The co-pending applications are: App. No.: 10/400,309 filed

3/27/03; App. No.: 10/136,890 filed 5/02/02; App. No.: 10/435,192 filed on 5/09/03; App. No.: 10/453,398 filed on 6/03/03; App. No.: 10/457,608 filed on 6/09/03; App. No.: 10/631,886 filed on 7/11/03; App. No.: 10/624,097 filed on 7/21/03; App. No.: 10/804,452 filed on 3/19/04; App. No.: 10/814,530 filed on 3/31/04; App. No.: 10/814,532 filed on 3/31/04; App. No.: 10/828,495 filed on 4/20/04; App. No.: 10/836,855 filed on 4/30/04; App. No.: 10/845,029 filed on 5/13/04; App. No.: 10/709,732 filed on 5/25/04; App. No.: 10/872,897 filed on 6/21/04; App. No.: 10/873,847 filed on 6/22/04; App. No.: 10/913,220 filed on 8/06/04; App. No.: 10/914,483 filed on 8/09/04; App. No.: 10/917,216 filed on 8/12/04; App. No.: 10/931,828 filed on 9/01/04; App. No.: 10/958,584 filed on 10/05/04; App. No.: 10/958,924 filed on 10/05/04; App. No.: 10/958,934 filed on 10/05/04; App. No.: 10/963,867 filed on 10/12/04; App. No.: 10/978,149 filed on 10/29/04; App. No.: 11/011,469 filed on 12/14/04.

This Information Disclosure Statement is being filed before receipt of the first Office action, so no fee is believed to be due.

The Commissioner is hereby authorized to charge any fees which may be required in connection with the filing, or to credit any overpayment to Deposit Account No. 50-0897, upon which account the undersigned has the authority to sign. **A duplicate copy of Forms PTO/SB/08 are enclosed.**

1/13/05

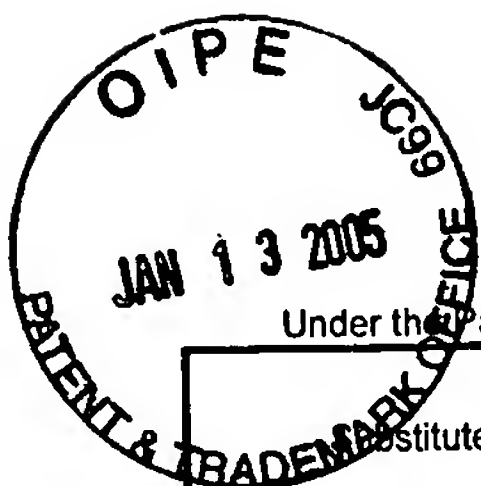
Date

Respectfully submitted,

ANDREWS KURTH LLP

By: _____

Nathan H. Calvert
Patent Reg. No. 48,990
111 Congress Ave., Suite 1700
Austin, Texas 78701
(512) 320-9200 Telephone
(512) 320-9292 Facsimile



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Substitute for form 1449/PTO

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Sheet 1 of 9

Complete if Known

Application Number	10/709,732
Filing Date	May 25, 2004
First Named Inventor	Russell Rapport, et al.
Art Unit	2811
Examiner Name	TBA
Attorney Docket Number	254-094-CIP4-CMEM

U. S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
		US-6,677,670 B2	01/13/2004	Kondo	
		US-6,660,561 B2	12/09/2003	Forthun	
		US-6,627,984 B2	09/30/2003	Bruce, et al.	
		US-6,620,651 B2	09/16/2003	He, et al.	
		US-6,600,222 B1	07/29/2003	Levarado	
		US-6,590,282 B1	07/08/2003	Wang, et al.	
		US-6,572,387 B2	06/03/2003	Burns, et al.	
		US-6,560,117 B2	05/06/2003	Moon	
		US-6,552,910 B1	04/22/2003	Moon, et al.	
		US-6,514,793 B2	02/04/2003	Isaak	
		US-6,486,544 B1	11/26/2002	Hashimoto	
		US-6,473,308 B2	10/29/2002	Forthun	
		US-6,465,893 B1	10/15/2002	Khandros, et al.	
		US-6,465,877 B1	10/15/2002	Farnworth, et al.	
		US-6,462,412 B2	10/08/2002	Kamei, et al.	
		US-6,446,158 B1	09/03/2002	Karabatsos	
		US-6,444,921 B1	09/03/2002	Wang, et al.	
		US-6,426,549 B1	07/30/2002	Isaak	
		US-6,426,240 B2	07/30/2002	Isaak	
		US-6,410,857 B1	06/25/2002	Gonya	

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³ -Number ⁴ -Kind Code ⁵ (if known)				
		EP 461-639(A)	12/18/1991	Kitano, et al.		
		JP 2003309246(A)	10/31/2003	Mitsuru		
		JP 2003086761(A)	03/20/2003	Iwao, et al.		
		JP 2003086760(A)	03/20/2003	Masataka, et al.		
		JP 2003037246(A)	02/07/2003	Masami		
		JP 2001332683(A)	11/30/2001	Toshiya		

Examiner Signature	Date Considered
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Substitute for form 1449/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(Use as many sheets as necessary)</i>				Complete if Known	
				Application Number	10/709,732
				Filing Date	May 25, 2004
				First Named Inventor	Russell Rapport, et al.
				Art Unit	2811
				Examiner Name	TBA
Sheet	2	of	9	Attorney Docket Number	254-094-CIP4-CMEM

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		Number-Kind Code ² (if known)			
		US-6,392,162 B1	05/21/2002	Karabatsos	
		US-6,376,769 B1	04/23/2002	Chung	
		US-6,368,896 B2	04/09/2002	Farnworth, et al.	
		US-6,360,433 B1	03/26/2003	Ross	
		US-6,351,029 B1	02/26/2002	Isaak	
		US-6,323,060 B1	11/27/2001	Isaak	
		US-6,300,679 B1	10/09/2001	Mukerji, et al.	
		US-6,281,577 B1	08/28/2001	Oppermann, et al.	
		US-6,266,252 B1	07/24/2001	Karabatsos	
		US-6,265,660 B1	07/24/2001	Tandy	
		US-6,262,895 B1	07/17/2001	Forthun	
		US-6,262,476 B1	07/17/2001	Vidal	
		US-6,233,650 B1	05/15/2001	Johnson, et al.	
		US-6,225,688 B1	05/01/2001	Kim, et al.	
		US-6,222,737 B1	04/24/2001	Ross	
		US-6,208,521 B1	03/27/2001	Nakatsuka	
		US-6,205,654 B1	03/27/2001	Burns	
		US-6,172,874 B1	01/09/2001	Bartilson	
		US-6,433,418	08/13/2002	Fujisawa, et al.	
		US-6,121,676	09/19/2000	Solberg	
		US-6,097,087	08/01/2000	Farnworth, et al.	

FOREIGN PATENT DOCUMENTS						
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		Country Code ³ -Number ⁴ -Kind Code ⁵ (if known)				
		JP 2001085592(A)	03/30/2001	Kenji		
		JP 2001077294(A)	03/23/2001	Katsumasa		

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				Application Number	10/709,732
				Filing Date	May 25, 2004
				First Named Inventor	Russell Rapport, et al.
				Art Unit	2811
				Examiner Name	TBA
Sheet	3	of	9	Attorney Docket Number	254-094-CIP4-CMEM

U. S. PATENT DOCUMENTS					
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		Number-Kind Code ² (if known)			
		US-6,072,233	06/06/2000	Corisis, et al.	
		US-6,028,365	02/22/2000	Akram, et al.	
		US-6,028,352	02/22/2000	Eide	
		US-6,025,642	02/15/2000	Burns	
		US-6,014,316	01/11/2000	Eide	
		US-5,953,215	09/14/1999	Karabatsos	
		US-5,949,657	09/07/1999	Karabatsos	
		US-5,926,369	07/20/1999	Ingraham, et al.	
		US-5,917,709	06/29/1999	Johnson, et al.	
		US-5,869,353	02/09/1999	Levy, et al.	
		US-5,835,988	11/10/1998	Ishii	
		US-5,828,125	10/27/1998	Burns	
		US-5,805,422	09/08/1998	Otake, et al.	
		US-5,801,437	09/01/1998	Burns	
		US-5,778,552	07/14/1998	Burns	
		US-5,776,797	07/07/1998	Nicewarner, Jr., et al.	
		US-5,654,877	08/05/1997	Burns	
		US-5,644,161	07/01/1997	Burns	
		US-5,642,055	06/24/1997	Difrancesco	
		US-5,631,193	05/20/1997	Burns	

FOREIGN PATENT DOCUMENTS						
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Examiner Signature		Date Considered	
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Sheet	4	of	9		

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		Number-Kind Code ² (if known)			
		US-5,612,570	03/18/1997	Eide, et al.	
		US-5,592,364	01/07/1997	Roane	
		US-5,572,065	11/05/1996	Burns	
		US-5,566,051	10/15/1996	Burns	
		US-5,561,591	10/01/1996	Burns	
		US-5,543,664	08/06/1996	Burns	
		US-5,541,812	07/30/1996	Burns	
		US-5,523,619	06/04/1996	McAllister, et al.	
		US-5,514,907	05/07/1996	Moshayedi	
		US-5,499,160	03/12/1996	Burns	
		US-5,493,476	02/20/1996	Burns	
		US-5,484,959	01/16/1996	Burns	
		US-5,479,318	12/26/1995	Burns	
		US-5,475,920	12/19/1995	Burns, et al.	
		US-5,455,740	10/03/1995	Burns	
		US-5,448,511	09/05/1995	Paurus, et al.	
		US-5,446,620	08/29/1995	Burns, et al.	
		US-5,428,190	06/27/1995	Stopperan	
		US-5,420,751	05/30/1995	Burns	
		US-5,402,006	03/28/1995	O'Donley	

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		Number-Kind Code ² (if known)			
		US-5,394,010	02/28/1995	Tazawa, et al.	
		US-5,377,077	12/27/1994	Burns	
		US-5,375,041	12/20/1994	McMahon	
		US-5,347,428	09/13/1994	Carson, et al.	
		US-5,343,075	08/30/1994	Nishino	
		US-5,313,097	05/17/1994	Haj-Ali-Ahmadi, et al.	
		US-5,311,401	05/10/1994	Gates, Jr. et al.	
		US-5,289,062	02/22/1994	Wyland	
		US-5,281,852	01/25/1994	Normington	
		US-5,279,029	01/18/1994	Burns	
		US-5,262,927	11/16/1993	Chia, et al.	
		US-5,261,068	11/09/1993	Gaskins, et al.	
		US-5,259,770	11/09/1993	Bates, et al.	
		US-5,247,423	09/21/1993	Lin, et al.	
		US-5,241,454	08/31/1993	Ameen, et al.	
		US-5,239,198	08/24/1993	Lin et al.	
		US-5,224,023	06/29/1993	Smith, et al.	
		US-5,222,014	06/22/1993	Lin	
		US-5,198,888	03/30/1993	Sugano, et al.	
		US-5,159,434	10/27/1992	Kohno, et al.	

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				Application Number	10/709,732
				Filing Date	May 25, 2004
				First Named Inventor	Russell Rapport, et al.
				Art Unit	2811
				Examiner Name	TBA
Sheet	6	of	9	Attorney Docket Number	254-094-CIP4-CMEM

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
		US-5,138,430	08/11/1992	Gow, 3rd, et al.	
		US-5,117,282	05/26/1992	Salatino	
		US-5,104,820	04/14/1992	Go, deceased, et al.	
		US-5,099,393	03/24/1992	Bentlage, et al.	
		US-5,041,015	08/20/1991	Travis	
		US-5,034,350	07/23/1991	Marchisi	
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		US-4,956,694	09/11/1990	Eide	
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Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³ -Number ⁴ -Kind Code ⁵ (if known)				

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		Number-Kind Code ² (if known)			
		US-4,733,461	03/29/1988	Nakano	
		US-4,696,525	09/29/1987	Coller, et al.	
		US-4,645,944	02/24/1987	Uya	
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		US-4,437,235	03/20/1984	McIver	
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		US-3,654,394	04/04/1972	Gordon	
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		US-2002/0139577 A1	10/03/2002	Miller	
		US-2002/0101261 A1	08/01/2002	Karabatsos	

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
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NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		WILLIAM R. NEWBERRY, Design Techniques for Ball Grid Arrays, Xynetix Design Systems, Inc., Portland, Maine, Published on the Internet.	
		Tessera Technologies, Inc. - Semiconductor Intellectual Property, Chip Scale Packaging - Website pages (3), Published on Internet.	
		Tessera Introduces uZ ä - Ball Stacked Memory Package for Computing and Portable Electronic Products, OYCE SMARAGDIS, Tessera Public Relations, SANDY SKEES, MCA PR (www.tessera.com/news_events/press_coverage.cfm); 2 figures that purport to be directed to the uZ ä - Ball Stacked Memory Package. Published 7/17/2002, San Jose, CA.	
		RON BAUER, Intel. "Stacked-CSP Delivers Flexibility, Reliability, and Space-Saving Capabilities", Spring 2002, Published on the Internet.	
		Tessera uZ Ball Stack Package. 4 figures that purport to be directed to the uZ - Ball Stacked Memory, Published on the Internet.	
		Chip Scale Review Online - An Independent Journal Dedicated to the Advancement of Chip-Scale Electronics. (Website 9 pages) FJELSTAD, JOSEPH, Pacific Consultants L.L.C., Published Jan 2001 on Internet.	
		Flexible Thinking: Examining the Flexible Circuit Tapes. (Website 2 pages) FJELSTAD, JOSEPH., Published 4/20/2000 on Internet.	
		Die Products: Ideal IC Packaging for Demanding Applications - Advanced packaging that's no bigger than the die itself brings together high performance and high reliability with small size and low cost. (Website 3 pages with 2 figures) LARRY GILG AND CHRIS WINDSOR. December 23, 2002. Published on Internet.	

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